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<u>L1</u>	(multi near1 thread\$4 or thread\$3) near5 (plur\$7 or multiple or two) and (replicat\$4 or duplicat\$4 or cop\$3 or identical) near4 thread\$3	3047	<u>L1</u>

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Computer Architecture, 2000. Proceedings of the 27th International Symposium on , 10-14 June 2000

Pages:25 - 36

[\[Abstract\]](#) [\[PDF Full-Text \(1412 KB\)\]](#) **IEEE CNF**
**2 A nested invocation suppression mechanism for active replication fault tolerant CORBA***Deron Liang; Chen-Liang Fang; Chyauhwa Chen; Fengyi Lin;*

Software Engineering Conference, 2002. Ninth Asia-Pacific , 4-6 Dec. 2002

Pages:117 - 125

[\[Abstract\]](#) [\[PDF Full-Text \(409 KB\)\]](#) **IEEE CNF**
**3 A nested invocation suppression framework for active replication fault tolerant CORBA***Deron Liang; Chen-Liang Fang; Chyauhwa Chen; JiChiang Tsai;*

Computer Software and Applications Conference, 2002. COMPSAC 2002.

Proceedings. 26th Annual International , 26-29 Aug. 2002

Pages:757 - 762

[\[Abstract\]](#) [\[PDF Full-Text \(417 KB\)\]](#) **IEEE CNF**
**4 A study of slipstream processors***Purser, Z.; Sundaramoorthy, K.; Rotenberg, E.;*

Microarchitecture, 2000. MICRO-33. Proceedings. 33rd Annual IEEE/ACM International Symposium on , 10-13 Dec. 2000

Pages:269 - 280

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